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FIG.1

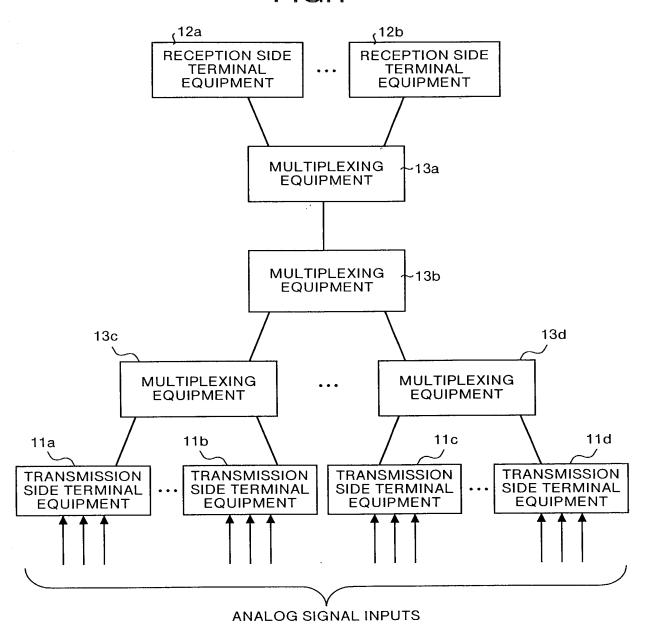
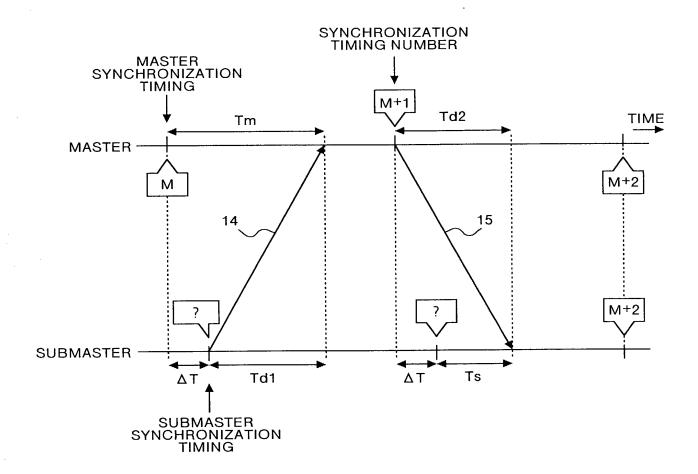
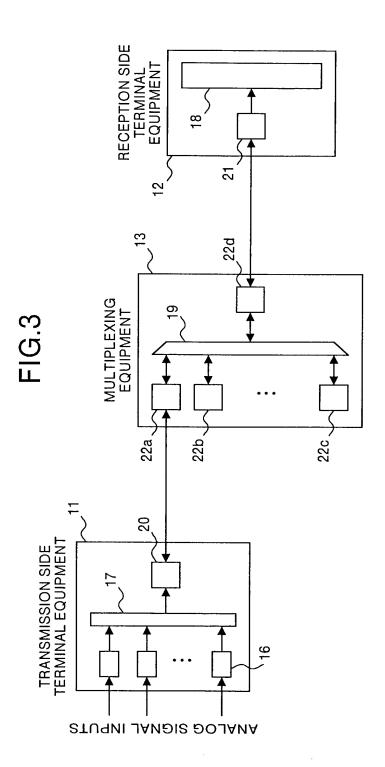


FIG.2

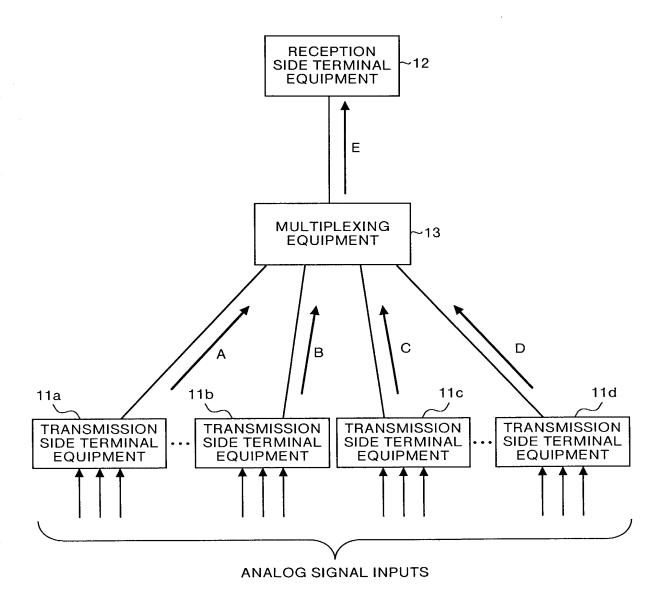


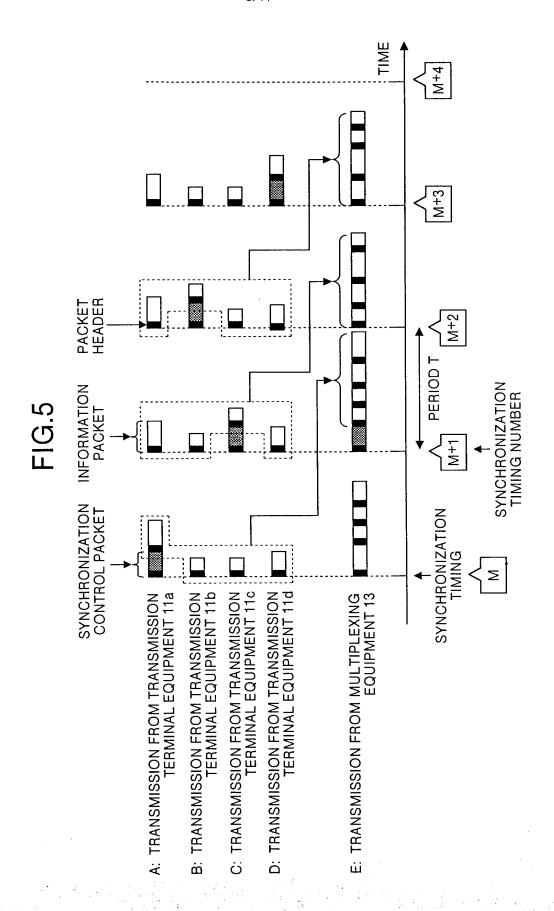


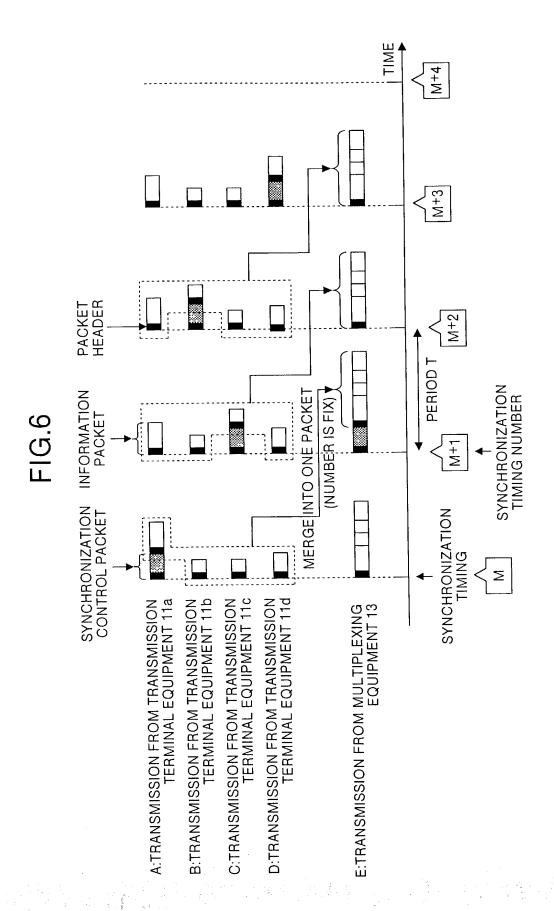
SUBSIDER OF CONTROL OF COURSE

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FIG.4







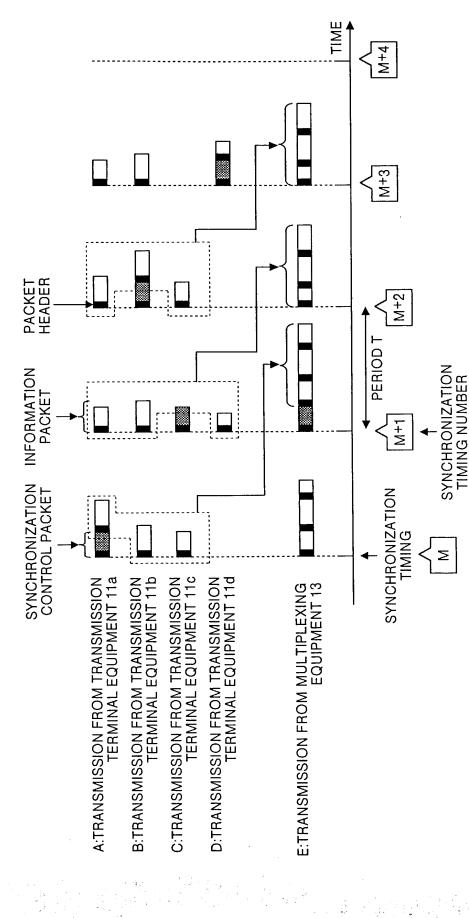
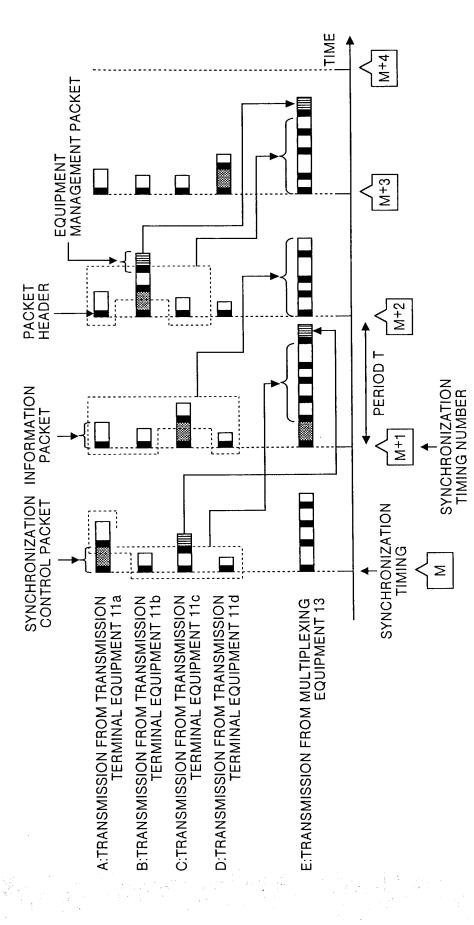


FIG.7



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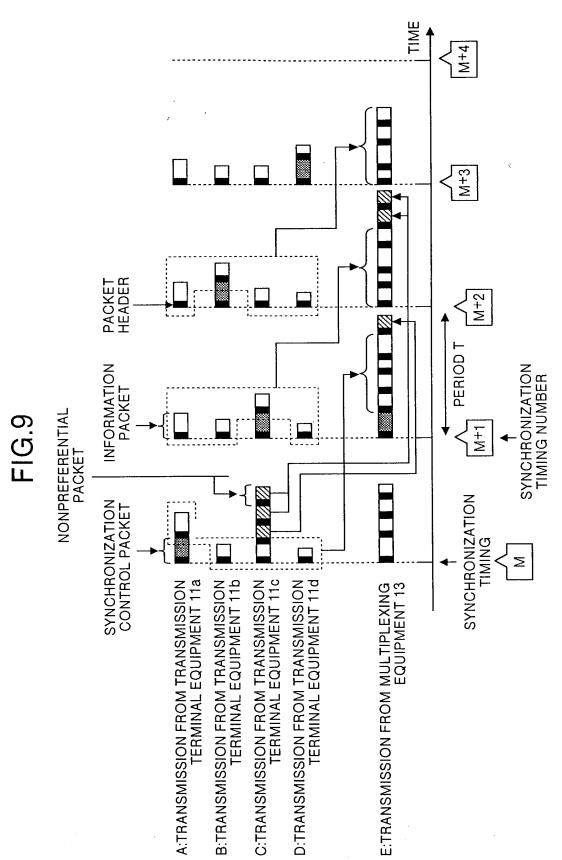
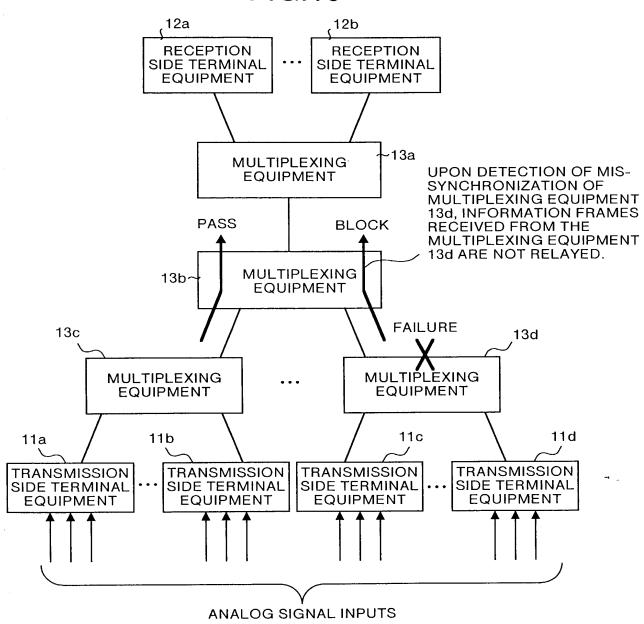


FIG.10



► ANALOG SIGNAL OUTPUT CH2 ► ANALOG SIGNAL OUTPUT CH1 ▼ ANALOG SIGNAL OUTPUT CHX RECEPTION SIDE TERMINAL EQUIPMENT TRANSMISSION SIDE TERMINAL EQUIPMENT ANALOG SIGNAL INPUT CH1-ANALOG SIGNAL INPUT CHX-ANALOG SIGNAL INPUT CH2-

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